

参考文献

- [1] 福田 晃, “並列オペレーティングシステム,” コロナ社.
- [2] 湯淺太一, 安村通晃, 中田登志之 編, “はじめての並列プログラミング,” 共立出版.
- [3] 笠原博徳, “並列処理技術,” コロナ社.
- [4] J. Bacon 著, 藤田, 篠田, 今泉 共訳, “並行分散システム,” Addison-Wesley Toppan.
- [5] 石川 裕, “コモディティハードウェアを用いた並列処理技術,” 情報処理, Vol.39, No.8, pp.784–791, 1998 Aug.
- [6] 緑川博子 編, “特集 計算機クラスタ,” 情報処理, Vol.39, No.11, pp.1071–1100, 1998 Nov.
- [7] J. Protic, M. Tomasevic, and V. Milutinovic, “Distributed Shared Memory: Concepts and Systems,” *IEEE Parallel and Distributed Technology*, Vol.4, No.2, pp.63–79, 1996 Summer.
- [8] Distributed Shared Memory Home Pages (URL <http://www.cs.umd.edu/~keleher/dsm.html>)
- [9] Liviu Iftode and Jaswinder Pal Singh, “Shared Virtual Memory: Progress and Challenges,” *Proceedings of the IEEE*, Vol.87, No.3, 1999 March.
- [10] MYRINET PERFORMANCE MEASUREMENTS (on line), (URL <http://www.myri.com/myrinet/performance/index.html>)
- [11] Richard B. Gillett, “MEMORY CHANNEL NETWORK FOR PCI,” *IEEE Micro*, pp.12–18, 1996 Feb.
- [12] Marco Fillo and Richard B. Gillet, “Architecture and Implementation of MEMORY CHANNEL2”, *DIGITAL Technical Journal*(on line), 28 Aug. 1997. (URL <http://www.digital.com/info/DTJP03/DTJP03HM.HTM>)
- [13] Al Geist, et al., PVM:Parallel Virtual Machine A Users' Guide and Tutorial for Networked Parallel Computing, MIT Press. also (URL <http://www.netlib.org/pvm3/book/pvm-book.html>)
- [14] MPI forum, MPI:A Message Passing Interface Standard, 1995. Also (URL <http://www.mpi-forum.org/docs/docs.html>) その他 (URL <http://phase.etl.go.jp/mpi/>) にも 詳細な情報あり .
- [15] MPICH-A Portable Implementation of MPI, (URL <http://www.mcs.anl.gov/Projects/mpi/mpich/index.html>)
- [16] OpenMP: A Proposed Industry Standard API for Shared Memory Programming, October 1997. OpenMP ホームページ (URL <http://www.openmp.org/>)
- [17] OpenMP Fortran Application Program Interface, October 1997. OpenMP ホームページ
- [18] C.D. Polychronopoulos and D.J. Kuck, “Guided Self-Scheduling: A Practical Scheduling Scheme for Parallel Supercomputers,” *IEEE Trans. on Computer*, Vol. C-36, No.12, 1987 Dec.

- [19] Rajiv Gupta, "The Fuzzy Barrier: A Mechanism for High Speed Synchronization of Processors," *Proc. of the Int'l Conf. on Architectural Support for Programming Languages and Operating Systems (ASPLOS III)*, pp.54–63, 1989.
- [20] T.E. Anderson, D.E. Celler, D.A. Patterson, et al., A Case for NOW(Networks of Workstations), *IEEE Micro*, pp.54–64, 1995 Feb.
- [21] D.E. Celler, et al., Parallel Computing on the Berkeley NOW, *JSPP'97*, pp.237–247, 1997.
- [22] S.Pakin, M.Lauria and A. Chien, "High Performance Messaging on Workstations: Illinois Fast Messages (FM) for Myrinet", *Supercomputing '95* (FM ホームページ URL <http://www-csag.cs.uiuc.edu/projects/comm/fm.html>)
- [23] High Performance Virtual Machines ホームページ (URL <http://www-csag.cs.uiuc.edu/projects/hpvm.html>)
- [24] Global Arrays (URL <http://www.emsl.pnl.gov:2080/docs/global/>)
- [25] Cluster Technologies at RWC (URL <http://www.rwcp.or.jp/people/mpslab/clusters/home.html>)
- [26] 手塚, 堀, 石川, "ワークステーションクラスタ用通信ライブラリ PM の設計と実装," *JSPP'96*, pp.41-48, 1996 June.
- [27] Parallel Programming Environment for Workstation Cluster (URL <http://www.rwcp.or.jp/lab/pdslab/score/scored/scored.html>)
- [28] Kai Li, "IVY: A Shared Virtual Memory System for Parallel Computing," *Proc. of Int'l Conf. on Parallel Processing*, pp.94–101, 1996.
- [29] C. Amza, et al., TreadMarks: Shared Memory Computing on Networks of Workstations, (URL <http://www.cs.rice.edu/~willy/papers/computer95.ps.gz>)
- [30] Cezary Dubnicki, Liviu Iftode, Edward W. Felten, and Kai Li, "Software Support for Virtual Memory-Mapped Communication," Proc. of the 10th Int'l Parallel Processing Symp., April 1996.
- [31] Matthias A. Blumrich, Kai Li, Richard Alpert, Cezary Dubnicki, and Edward W. Felten, "Virtual Memory Mapped Network Interface for the SHRIMP Multicomputer," Proc. of Int'l Symp. on Comp. Arch., pp.142–153, 1994.
- [32] Leonidas I. Kontothanassis and Micheal L. Scott, "Distributed Shared Memory for New Generation Networks," Tech. Rep. #578, Dep. of Computer Science, Univ. of Rochester, March 1995.
- [33] Leonidas I. Kontothanassis and Micheal L. Scott, "Software Cache Coherence for Large Scale Multiprocessors," Proc. of the 1st Int'l Symp. on High Performance Computer Architecture, pp. 286–295, Jan. 1995.
- [34] Mark D. Hill, James R. Larus, and David A. Wood, "Tempest: A Substrate for Portable Parallel Programs," Proc. of Joint Symp. on Parallel Processing, pp.123–128, May 1995.
- [35] Tom Lovett and Russell Clapp, "STiNG: A CC-NUMA Computer System for the Commercial Marketplace," Proc. of Int'l Symp. on Comp. Arch., pp.308–317, June 1996.

- [36] 安生, 中條, 小野, 工藤, 他, “分散共有メモリを持つ WS クラスタ : JUMP-1/3,” *JSPP'97*, pp.321–328, 1997.
- [37] Wolf-Dietrich Weber, et al., “The Mercury Interconnect Architecture: A Cost-Effective Infrastructure for High-Performance Servers,” *Proc. of Int'l Symp. on Comp. Arch.*, pp.98–107, 1997.
- [38] Wolf-Dietrich Weber, et al., “The Synfinity Interconnect Architecture: A Cost-Effective Infrastructure for High-Performance Servers,” (URL <http://www.fjst.com/products/synfinity numa/abstract/>)
- [39] S. Mori, et al., “A Distributed Shared Memory Multiprocessor:ASURA — Memory and Cache Architectures —,” *Proc. of SUPERCOMPUTING 1993*, pp.740–749, 1993
- [40] V. Krishnaswamy, et al., “The Architecture of a LINDA coprocessor,” *Proc. of Int'l Symp. on Comp. Arch.*, pp.240–249, June 1988.
- [41] D.J.Scales, K. Gharachorloo, and C.A.Thekkath, “Shasta: A Low Overhead, Software-Only Approach for Supporting Fine-Grain Shared Memory,” *Proc. of the Int'l Conf. on Architectural Support for Programming Languages and Operating Systems (ASPLOS VII)*, pp.174–185, 1996 Oct.
- [42] Liviu Iftode, Jaswinder Pal Singh, and Kai Li, “Scope Consistency : A Bridge between Release Consistency and Entry Consistency,” *Proc. of the ACM Symp. on Parallel Algorithms, and Architecture*, pp.277–287, 1996.